# BEST AVAILABLE COPY

# PATENT ABSTRACTS OF JAPAN

(11)Publication number:

08-110528

(43) Date of publication of application: 30.04.1996

(51)Int.CI.

G02F 1/136

G02F 1/1345

(21)Application number: 06-

(71)Applicant : CASIO COMPUT CO

246165

LTD

(22)Date of filing:

12.10.1994 (72) Inventor: ONAKA EIICHI

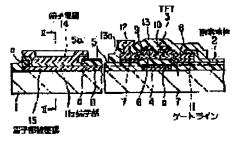
## (54) ACTIVE MATRIX PANEL AND ITS PRODUCTION

(57)Abstract:

PURPOSE: To prevent the occurrence of the conduction defect of gate line terminal parts and to improve the yield of production by providing gate lines with coating films consisting of the same transparent conductive films as the transparent conductive films of pixel electrodes on their terminal parts and forming terminal electrodes thereon.

CONSTITUTION: A transparent substrate

1 is provided thereon with the plural pixel electrodes 2, plural thin—film transistors (TFTs) 3 respectively corresponding to the pixel electrodes 2 and the gate lines



11 and data lines 12 for supplying gate signals and data signals to the TFTs 3. Further, the substrate is provided with interlayer insulating films 13 which cover the TFT3 and correspond to the wiring regions of the data lines 12 and the data lines 12 are connected to the drain electrodes 9 of the TFTs 3 in their contact holes 13a. On the other hand, the terminal parts 1a of the gate lines 11 are exposed by openings of the gate insulating films 5. These terminal parts 11a are provided thereon with terminal part coating films 15 which consist of the same transparent conductive films as the transparent conductive films of the pixel electrodes 2 and cover the entire surface of the terminal parts

BEST AVAILABLE COPY

11a. The terminal electrodes 14 connected to gate side driving circuits are formed thereon.

### LEGAL STATUS

[Date of request for examination] 09.10.2001 [Date of sending the examiner's 21.10.2003 decision of rejection] [Kind of final disposal of application other than the examiner's decision of rejection or application converted registration] Date of final disposal for application] [Patent number] [Date of registration] [Number of appeal against examiner's decision of rejection] [Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office

# 일본공개특허공보 평08-110528호(1996.04.30) 1부.

[첨부그림 1]

(10)日本四种野庁 (1 P)

(2) 公開特許公報(A)

(11)界所出線公開海外 特開平8-110528

(49 公前日 平成 8 年(1996) 4 月30日

GI) Int.CL\* COSE 1/196 1/1945

**#9626** か内差理会分 500

P i

技术表示证明

等型储存 未被求 附收型の数4 OL (全 9 頁)

(21) 出版青舟 (20) [20] [3]

**特据平8**—268165

平成8年(1980)19月12日

(71)出版人 000001443 から対計算機能の発生

東京都曾在四個台2丁日6巻1号 (72)発明者 周中 珠一

**本新鄉縣共央社八王子89人**所內

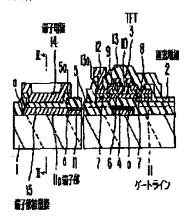
竹心代理人 外阻士 粉江 武道

(54) 【発情の名字】 アクティブマトリックスパネルおよびその提及方法

(37) (変わ)

(日的) 華通中にゲートラインの松子書分に伝道不良が **発生するのを防いで砂塩水管を向上させることができる** アクティブマトリックスパネルを接供する。 【構成】ゲートライン11の総子が116の上に、答案

**电径2 と同じ途明導電鉄からなる電子部被復展 1 5 を設** け、その上に細子を施り4色形成した。



9-1